Methods and Apparatus for Early Loop Bottom Detection in Digital Signal **Processors** Mayer, Christopher Docket No.: A0312.70524US00

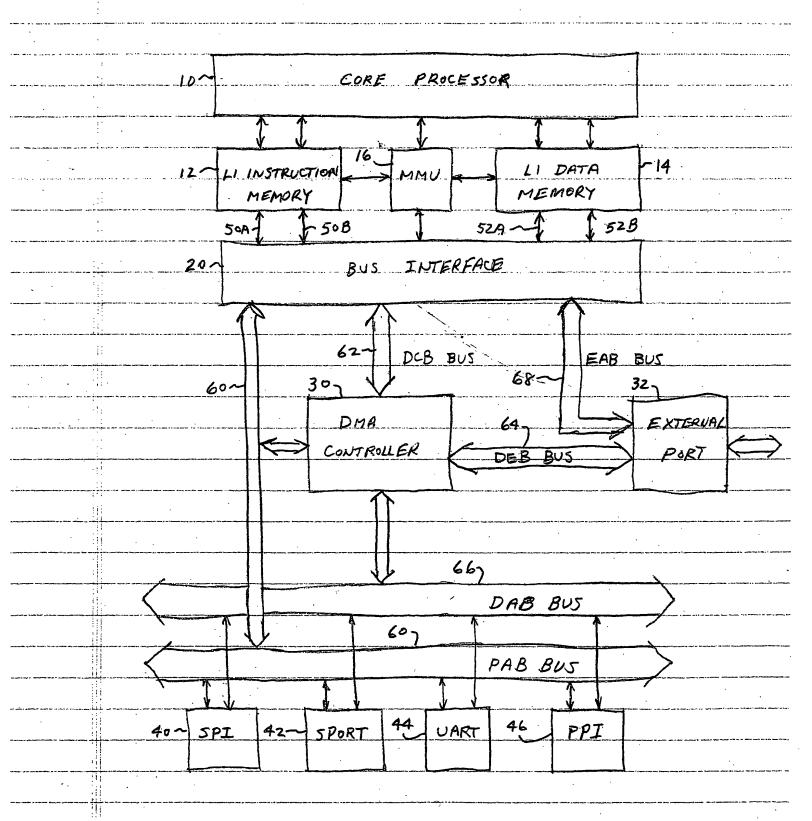
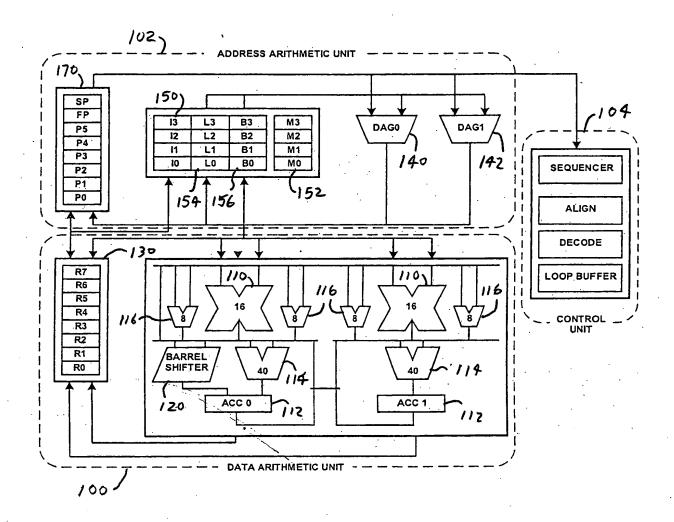


FIG. 1

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F16.2

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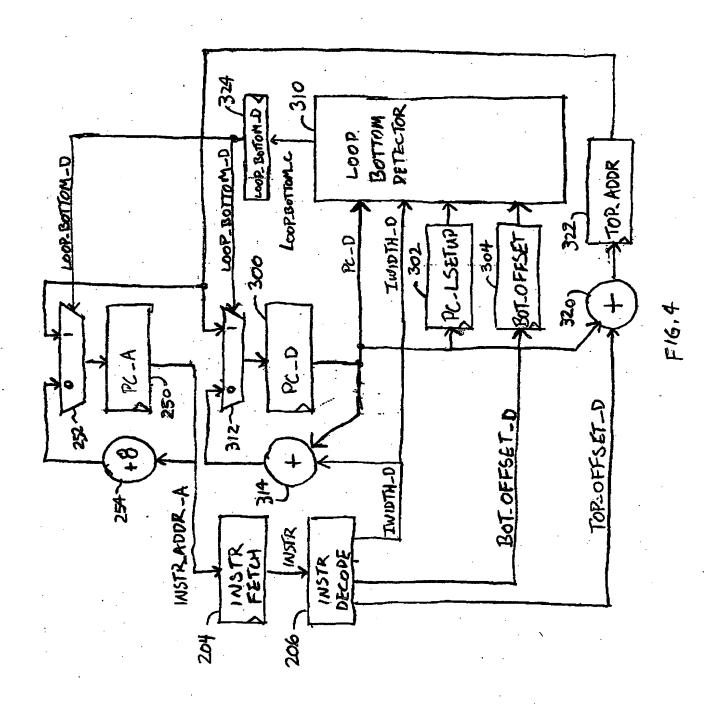
	·	1
218)	WB	
2/6)	Ex4	WB
214)	Ex3	Ex4
212)	EX2	Ex3
210)	EX1	EX2
2087	Addr Calc	Ex1
206)	Instr Decode	Addr Calc
204	Instr Fetch 3	Instr Decode
202)	Instr Fetch 2	Instr Fetch 3
2007	Instr Fetch 1	Instr Fetch 2
1		instr Fetch 1

F16,3

Methods and Apparatus for Early Loop Bottom Detection in Digital Signal

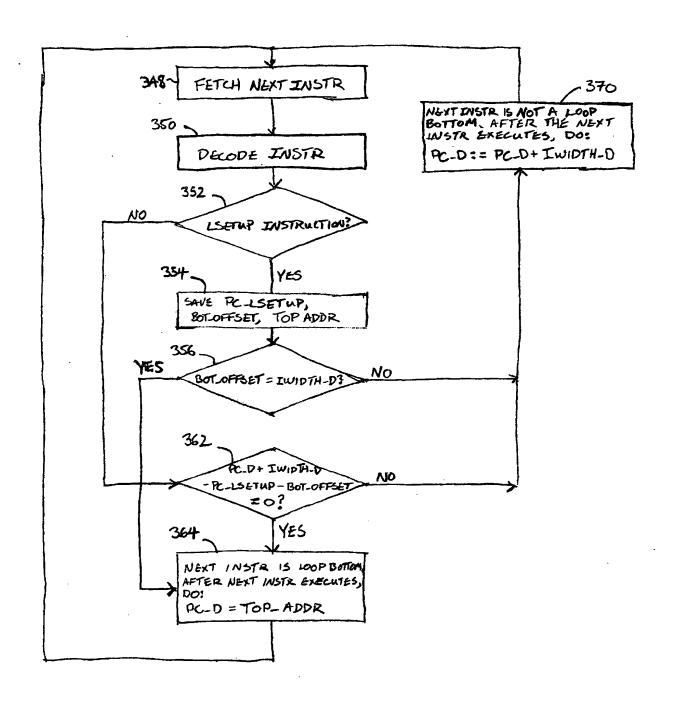
Processors

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Methods and Apparatus for Early Loop Bottom Detection in Digital Signal Processors

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F16,5

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